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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/792,366	03/03/2004	Chih-Chiang Su	10113881	4262
34283	7590	11/03/2006	EXAMINER	
QUINTERO LAW OFFICE 1617 BROADWAY, 3RD FLOOR SANTA MONICA, CA 90404			VU, PHU	
			ART UNIT	PAPER NUMBER
			2871	

DATE MAILED: 11/03/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.		Applicant(s)	
	10/792,366		SU, CHI-CHIANG	
	Examiner		Art Unit	
	Phu Vu		2871	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 10 August 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) 9-16 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-8 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

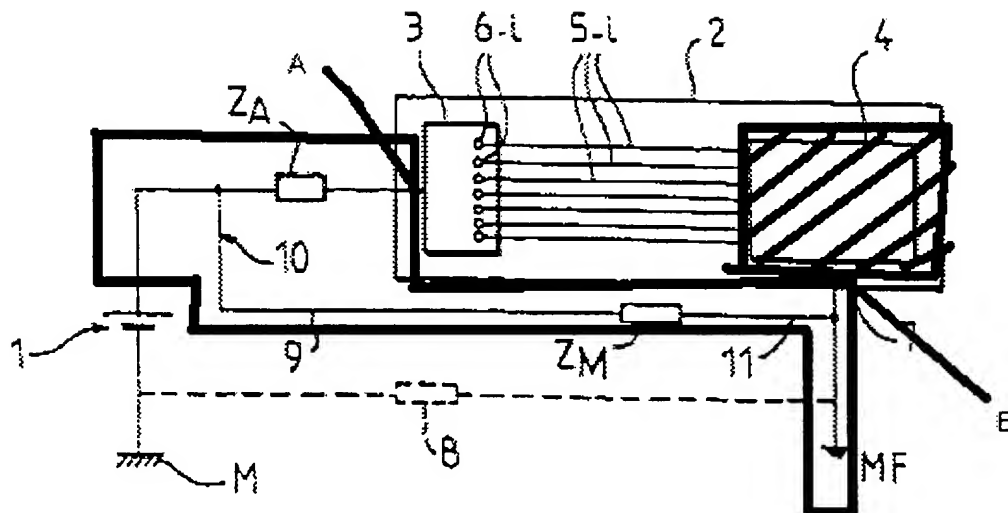
DETAILED ACTION

Election/Restrictions

Applicant's election without traverse of claims 1-8 in the reply filed on 8/10/2006 is acknowledged.

Response to Arguments

Applicant's arguments filed 4/24/06 have been fully considered but they are not persuasive. Regarding claims 1-8, application has sought clarification as what exactly is considered the surrounding portion and has also argued that the rejection as presented does not meet the limitations of the claim particularly the limitation of wires of the display module are schemed between the central portion and anti ESD wire. The central portion is roughly the shaded area (element 4 in the figure below). The surrounding portion is merely element 2 with the exception of the shade area (element 4). Furthermore, element 9, is considered the anti-ESD wire (denoted by the unshaded outlined area). Points A and B are where the anti-ESD wire is disposed on the surrounding portion. Note element 9 is considered to be all the wiring including as Z_A and Z_M are merely "impedance points" when viewed from ground and do not indicate any type of load aside from that caused by the wire itself (see column 3 lines 5-10) therefore both "sides" of Z_A and Z_M are considered a part of the anti-ESD wire. Therefore, it can be clearly seen that wires 5-1 are "schemed" between anti-ESD wire (unshaded outlined are) and central portion (shaded area). Applicant's arguments are not considered persuasive.



The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

**Claim 1-6 are rejected under 35 U.S.C. 102(b) as being anticipated by Huynh
US 6317172.**

Regarding claim 1, Huynh teaches an electronic device comprising: a parinted circuit board including a ground layer (fig. 1 element M), a liquid crystal display module (4) disposed on the printed circuit board, a surrounding portion (2), an anti-ESD wire (9) disposed on the surrounding portion, wires of the display module (5-I) are schemed between the anti ESD wire and the central portion, and the first contact is coupled to the

anti-ESD module (7) so that ESD in the LCD module is ground via the anti-ESD wire and the first contact, and a controller disposed on the PCB and coupled to the LCD display module for resetting at a predetermined interval

Regarding claim 2, the reference shows a first wire connecting the first contact (7) to the ground (MF).

Regarding claim 3, the reference shows the LCD module includes a plurality of second contacts and the first contact is located outside of the second contacts (5-l).

Regarding claim 4, the reference shows a second wire connecting one of the second contacts and the controller (see wire 5-i).

Regarding claim 5, the reference shows the central portion being the LCDM (4).

Regarding claim 6, the reference shows the surrounding portion of the LCDM is a circuit layout region of the LCDM (see fig. 1).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 7 is rejected under 35 U.S.C. 103(a) as being unpatentable over Huynh in view of Kim US 5517342.

Regarding claims 7, , Huynh teaches all the limitations of claims 7, 11, and 15 except an ITO wiring. Kim teaches ITO as a replacement for traditional aluminum wiring for display applications such as LCDs because of its transparency (column 5 lines 10-15). Therefore, at the time of the invention, it would have been obvious to one of ordinary skill in the art to apply ITO wiring in display application because of its transparency.

Claim is 8 rejected under 35 U.S.C. 103(a) as being unpatentable over Huynh in view of Yuuki et al US 6181071.

Regarding claim 8 Park and Cha disclose all the limitations of the claim except wire widths of .15 mm to .35 mm. Yuuki discloses a wire with of .1 mm - 1 mm to serve as a shield against electromagnetic waves (See column 8 lines 22-30). The MPEP section 2144.01 states In the case where the claimed ranges "overlap or lie inside ranges disclosed by the prior art" a prima facie case of obviousness exists. In re Wedheim, 541 F.2d 257, 191 USPQ 90 (CCPA 1976). Therefore, at the time of the invention, it would have been obvious to use wire widths of .15 to .35 mm to serve as electromagnetic shielding.

Conclusion


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phu Vu whose telephone number is (571)-272-1562. The examiner can normally be reached on 8AM-5PM M-F.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571)-272-1787. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Phu Vu
Examiner
AU 2871


ANDREW SCHECHTER
PRIMARY EXAMINER